

SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520URT	INTEL	5520	SMTL0047	PASS

Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG1027RD351293-HB	CAS Latency:	C999
Memory Capacity:	8GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	IDT IDTSSTE32882 HLBKAG
DRAM Part Number:	H5TQ2G43BFR-H9C	PLL P/N:	N/A
PCB Part Number:	1006-1EC	Dual Die Package:	MONOLITHIC
PCB Layer Count:	8	Comment:	

MINIMUM SYSTEM INFORMATION

System Name:	S5520URT
Serial Number:	V906MP0318 R
BIOS Version:	AMI S5500.86B.01.00.0049
Processor Code:	INTEL XEON X5670
OS Memory Size:	48GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

MAXIMUM SYSTEM INFORMATION

System Name:	S5520URT
Serial Number:	V906MP0318 R
BIOS Version:	AMI S5500.86B.01.00.0049
Processor Code:	INTEL XEON X5670
OS Memory Size:	96GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN

Voltage Margining Info

Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	9/30/2010		
Stop Date:	10/5/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		

Test Status: **PASS**

TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	10/1/2010		
Stop Date:	10/6/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		

Test Status: **PASS**

Serial Number List

1) SFR2010244568574	2) SFR2010244568656	3) SFR2010246665756	4) SFR2010236179946
5) SFR2010239325692	6) SFR2010245617150	7) SFR2010236179997	8) SFR2010237228573
9) SFR2010236179967	10) SFR2010247714311	11) SFR2010238277119	12) SFR2010241422842
13)	14)	15)	16)

CONTACT INFORMATION

Contact: Arthur Sainio Phone: 1-510-624-8126 e-mail: arthur.sainio@smartm.com

Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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