

# SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520HCR	INTEL	INTEL 5520	SMTL0030	PASS

### Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG1027RD351293HB	CAS Latency:	C999
Memory Capacity:	8GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	IDT IDTSSTE32882-HLBAKG
DRAM Part Number:	H5TQ2G43BFR-H9C	PLL P/N:	N/A
PCB Part Number:	PH5B240NESH3RE	Dual Die Package:	
PCB Layer Count:	8	Comment:	

### MINIMUM SYSTEM INFORMATION

System Name:	S5520HCR
Serial Number:	QSHN00401201
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5550
OS Memory Size:	48GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

### MAXIMUM SYSTEM INFORMATION

System Name:	S5520HCR
Serial Number:	QSHN00401201
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5550
OS Memory Size:	96GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

### PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN
<b>Voltage Margining Info</b>	
Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

### TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	8/9/2010		
Stop Date:	8/11/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

### TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	8/10/2010		
Stop Date:	8/13/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

### Serial Number List

1) SFR2010072300008	2) SFR2010072300009	3) SFR2010072300018	4) SFR2010072300010
5) SFR2010072300006	6) SFR2010072300007	7) SFR2010072300017	8) SFR2010072300004
9) SFR2010072300012	10) SFR2010072300014	11) SFR2010072300005	12) SFR2010072300011
13)	14)	15)	16)

### CONTACT INFORMATION

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Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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