

SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520SCR	INTEL	INTEL 5520	SMTL0067	PASS

Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG2567RD312893HT	CAS Latency:	C999
Memory Capacity:	2GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	IDT IDTSSTE32882 HLBKAG
DRAM Part Number:	H5TQ1G83TFR-H9C	PLL P/N:	N/A
PCB Part Number:	PH5AG240NEBHB2RB	Dual Die Package:	MONOLITHIC
PCB Layer Count:	10	Comment:	

MINIMUM SYSTEM INFORMATION

System Name:	S5520SCR
Serial Number:	QSHV00800136
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5650
OS Memory Size:	12GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

MAXIMUM SYSTEM INFORMATION

System Name:	S5520SCR
Serial Number:	QSHV00800136
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5650
OS Memory Size:	24GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN

Voltage Margining Info

Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	11/29/2010	Stop Date:	12/1/2010
HPL Test Status:	PASS	DDR Speed:	1333MHz
Summary Min:	P	Summary Max:	P

Test Status:	PASS
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TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	11/30/2010	Stop Date:	12/2/2010
HPL Test Status:	PASS	DDR Speed:	1333MHz
Summary Min:	P	Summary Max:	P

Test Status:	PASS
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Serial Number List

1) SFR201002000088	2) SFR201002000087	3) SFR201002000093	4) SFR201002000092
5) SFR201002000090	6) SFR201002000085	7) SFR201002000086	8) SFR201002000094
9) SFR201002000084	10) SFR201002000091	11) SFR201002000089	12) SFR201002000095
13) SFR201002000082	14) SFR201002000083	15)	16)

CONTACT INFORMATION

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Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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