

SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520HC	INTEL	INTEL 5520	SMTL0020	PASS

Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG1027RD351293-HB	CAS Latency:	C999
Memory Capacity:	8GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	INPHI SSTE32882LV-GS02
DRAM Part Number:	H5TQ2G43BFA-H9C	PLL P/N:	N/A
PCB Part Number:	1004-2AC	Dual Die Package:	MONOLITHIC
PCB Layer Count:	8	Comment:	

MINIMUM SYSTEM INFORMATION

System Name:	S5520HC
Serial Number:	31550MB0000
BIOS Version:	PHOENIX-SMART 8011.0006
Processor Code:	INTEL XEON X5550
OS Memory Size:	48GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

MAXIMUM SYSTEM INFORMATION

System Name:	S5520HC
Serial Number:	31550MB0000
BIOS Version:	PHOENIX-SMART 8011.0006
Processor Code:	INTEL XEON X5550
OS Memory Size:	96GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN
Voltage Margining Info	
Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	6/23/2010		
Stop Date:	6/28/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	6/24/2010		
Stop Date:	6/27/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

Serial Number List

1) SFR2010042954044	2) SFR2010042954033	3) SFR2010042954037	4) SFR2010042954042
5) SFR2010042954046	6) SFR2010042954040	7) SFR2010042954039	8) SFR2010042954038
9) SFR2010042954045	10) SFR2010042954043	11) SFR2010042954036	12) SFR2010042954041
13)	14)	15)	16)

CONTACT INFORMATION

Contact: Arthur Sainio Phone: 1-510-624-8126 e-mail: arthur.sainio@smartm.com

Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

WWW.SMARTM.COM