

SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520URT	INTEL	INTEL 5520	SMTL0075	PASS

Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG5127RD325693-HT	CAS Latency:	C999
Memory Capacity:	4GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	INPHI INSSTE32882LV-GS02
DRAM Part Number:	H5TQ1G43TFR-H9C	PLL P/N:	N/A
PCB Part Number:	1002-2DC	Dual Die Package:	
PCB Layer Count:	8	Comment:	

MINIMUM SYSTEM INFORMATION

System Name:	S5520URT
Serial Number:	BZUB02700996
BIOS Version:	AMI S5500.86B.01.00.0049
Processor Code:	INTEL XEON X5670
OS Memory Size:	24GB
OS Version:	LINUX SMP 2.6.36
Test Version:	HPL XLINPAK 10.3.0 MKL
Comment:	

MAXIMUM SYSTEM INFORMATION

System Name:	S5520URT
Serial Number:	BZUB02700996
BIOS Version:	AMI S5500.86B.01.00.0049
Processor Code:	INTEL XEON X5670
OS Memory Size:	48GB
OS Version:	LINUX SMP 2.6.36
Test Version:	HPL XLINPAK 10.3.0.MKL
Comment:	

PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN
Voltage Margining Info	
Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:			12/14/2010
Stop Date:			12/17/2010
HPL Test Status:			PASS
DDR Speed:			1333MHz
Summary Min:			P
Summary Max:			X P
Test Status:			PASS

TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:			12/16/2010
Stop Date:			12/20/2010
HPL Test Status:			PASS
DDR Speed:			1333MHz
Summary Min:			P
Summary Max:			P
Test Status:			PASS

Serial Number List

1) SFR20100401687282	2) SFR20100401687289	3) SFR20100401687287	4) SFR20100401687288
5) SFR20100401687284	6) SFR20100401687291	7) SFR20100401687290	8) SFR20100401687283
9) SFR20100401687292	10) SFR20100401687286	11) SFR20100401687285	12) SFR20100401687293
13)	14)	15)	16)

CONTACT INFORMATION

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Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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