

SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520HCR	INTEL	INTEL 5520	SMTL0087	PASS

Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG2567RV312893-HT	CAS Latency:	C999
Memory Capacity:	2GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	HYNIX	Register P/N:	IDT IDTSSTE32882 HLBKAG
DRAM Part Number:	H5TC1G83TFR-H9A-C	PLL P/N:	N/A
PCB Part Number:	0946-4DC	Dual Die Package:	MONOLITHIC
PCB Layer Count:	8	Comment:	

MINIMUM SYSTEM INFORMATION

System Name:	S5520HCR
Serial Number:	QSHN00401201
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5570
OS Memory Size:	12GB
OS Version:	LINUX SMP 2.6.36-SMART
Test Version:	HPL XLINPAK 10.3.0
Comment:	

MAXIMUM SYSTEM INFORMATION

System Name:	S5520HCR
Serial Number:	QSHN00401201
BIOS Version:	AMI S5500.86B.01.00.0045
Processor Code:	INTEL XEON X5570
OS Memory Size:	24GB
OS Version:	LINUX SMP 2.6.36-SMART
Test Version:	HPL XLINPAK 10.3.0
Comment:	

PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN
Voltage Margining Info	
Volt %	+/-3%
Volt Low	1.31 volt
Volt High	1.39 volt

TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	1/20/2011		
Stop Date:	1/24/2011		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	1/21/2011		
Stop Date:	1/25/2011		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

Serial Number List

1) SFR2010042953734	2) SFR2010042953740	3) SFR2010042953728	4) SFR2010042953729
5) SFR2010042953733	6) SFR2010042953739	7) SFR2010042953730	8) SFR2010042953736
9) SFR2010042953735	10) SFR2010042953738	11) SFR2010042953737	12) SFR2010042953741
13)	14)	15)	16)

CONTACT INFORMATION

Contact: Arthur Sainio Phone: 1-510-624-8126 e-mail: arthur.sainio@smartm.com

Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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